

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of :  
REDD, et al. : Group Art Unit: Unknown  
Serial No.: Not yet assigned : Examiner: Unknown  
Filed: Herewith :  
Title: Semiconductor Component and Method of Manufacturing Same

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached hereto is a completed Form PTO-1449 listing patents, publications, applications, or other information, which the applicant believes may be material to the examination of the subject patent application, with copies of each such item enclosed herewith. It is requested that the cited patents, publications, applications, or other information be made of record in the examination of this application. The filing of this information disclosure statement is not to be construed as an admission that the information cited in the statement is, or is considered to be, material to the patentability of the invention claimed in the above-identified application.

This information disclosure statement is being filed (1) within three months of the filing date of a national application other than a continued prosecution application, (2) within three months of the date of entry of the national stage, (3) before the mailing of a first Office Action on the merits, or (4) before the mailing of a first Office Action after the filing of a request for

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continued examination. Accordingly, no fee is required. The commissioner is, however, authorized to charge any fee deficiency required by this paper or credit any overpayment to Deposit Account No. 02-4467.

No representation is made that a search has been performed. No admission is made that the information cited in this statement is, or is considered to be, material to the patentability of the subject patent application.

Respectfully submitted,

Dated: June 27, 2003

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**CERTIFICATE OF EXPRESS MAILING UNDER 37 C.F.R. 1.10.**

I hereby certify that this document (and any as referred to as being attached or enclosed) is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" service, mailing label No. **EL452287954US** on **June 27, 2003** and addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Thomas LeVance  
Printed Name: Thomas LeVance

FORM PTO-1449      U.S. DEPARTMENT OF COMMERCE  PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use several sheets if necessary)	ATTY. DOCKET NO.	SERIAL NO
	137798	
	APPLICANT Redd, et al.	
	FILING DATE June 27, 2003	GROUP

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,831,320	11/3/98	Kwon, et al.			
	6,033,948	3/7/00	Kwon, et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date Pertinent Pages, Etc.)

		Sakura, et al.; 100W L-Band GaAs Power FP-HFET Operated at 30V; 2000 IEEE MTT-S Digest, pp. 1715-1718.

EXAMINER

DATE  
CONSIDERED

EXAMINER: Initial citation considered. Draw line through citation if not in the conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449)